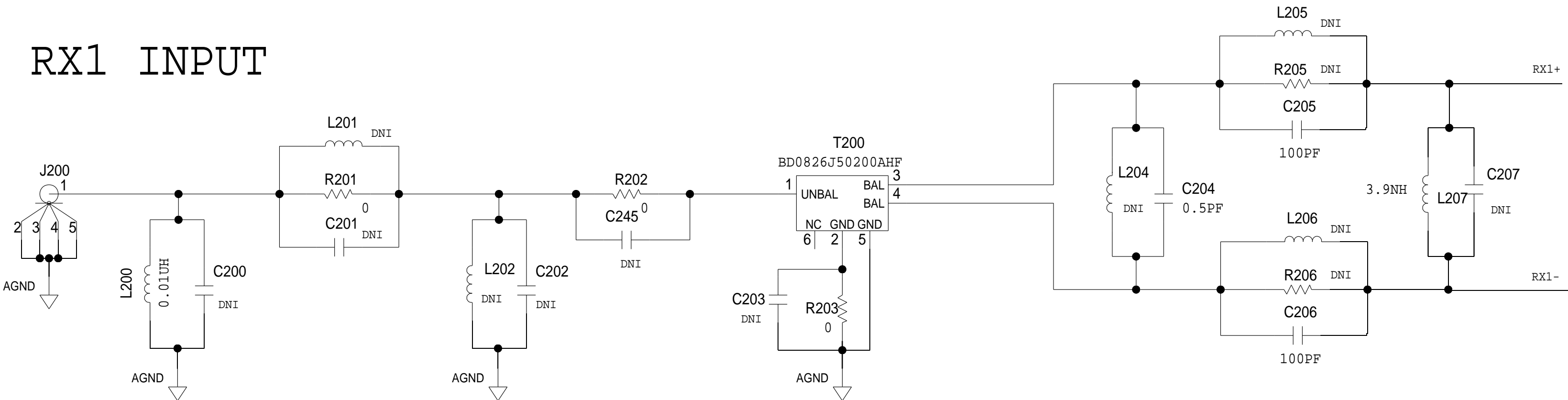


[illegible]

RF RX / ORX / SNIFFER INPUTS

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

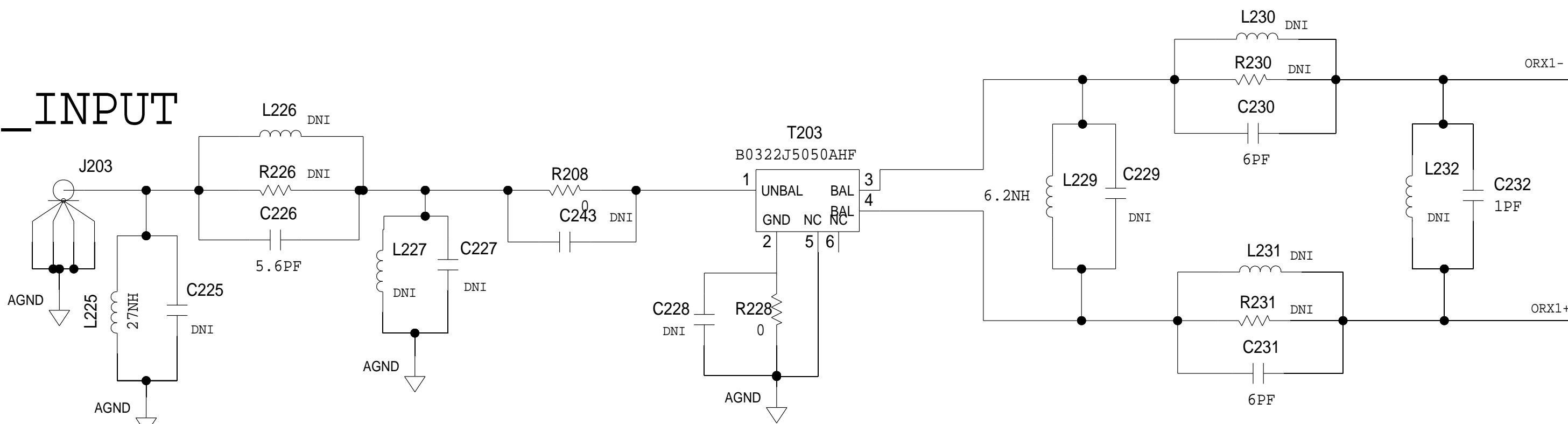
RX1 INPUT



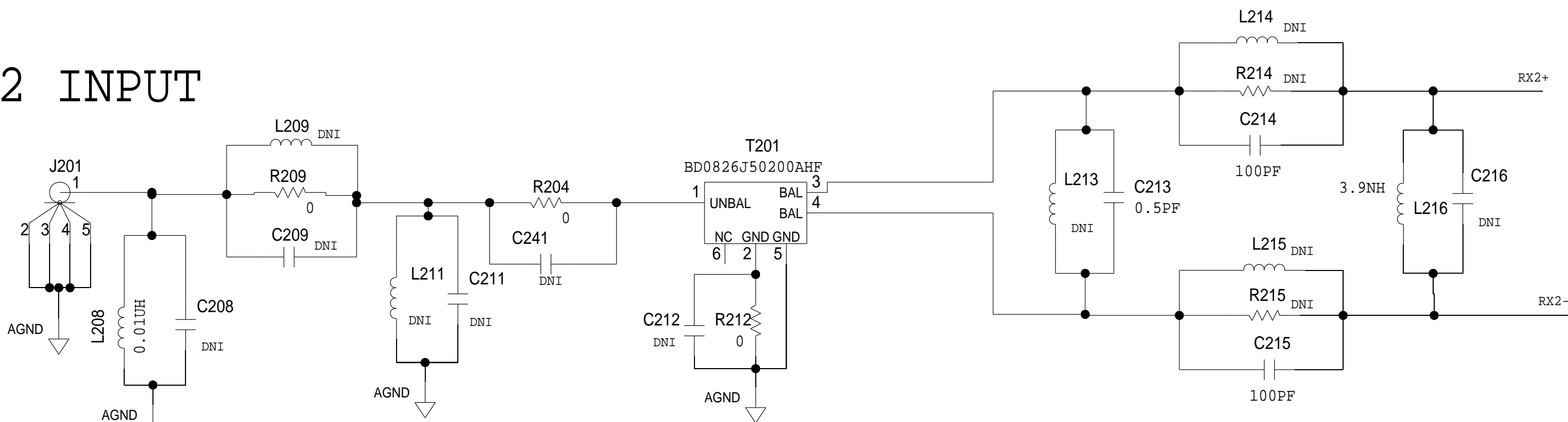
NOTE

EACH LC, RC OR RLC CLUSTER REPRESENTS A SINGLE PAD WHICH CAN BE POPULATED BY ONE OR NONE OF THE R,L OR C COMPONENTS.

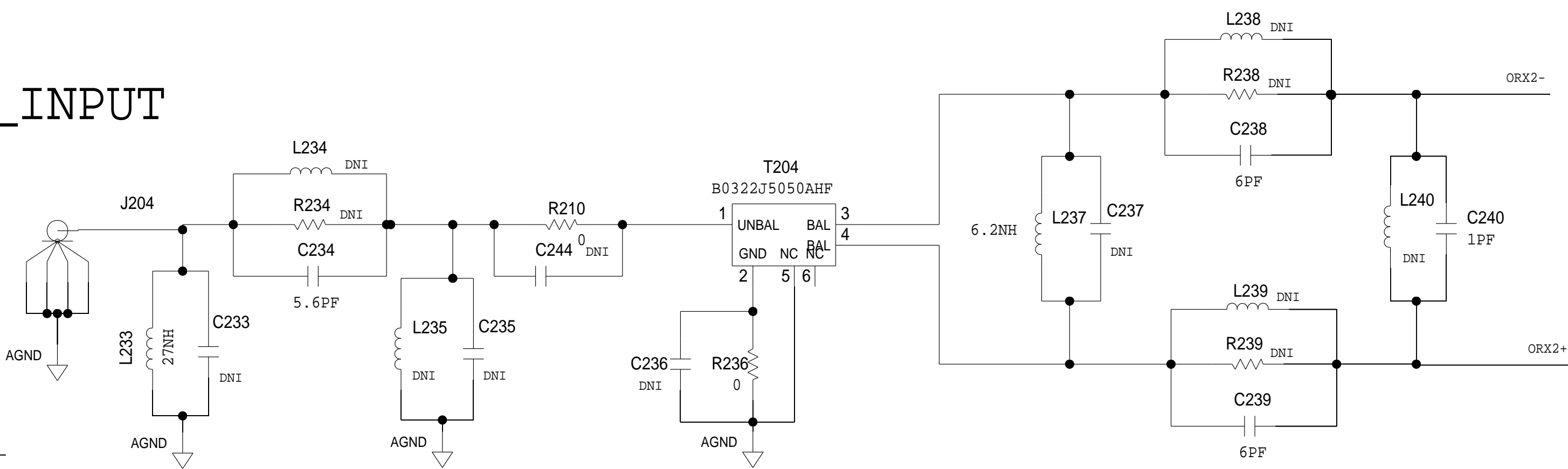
ORX1_INPUT



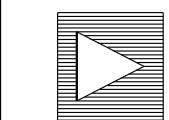
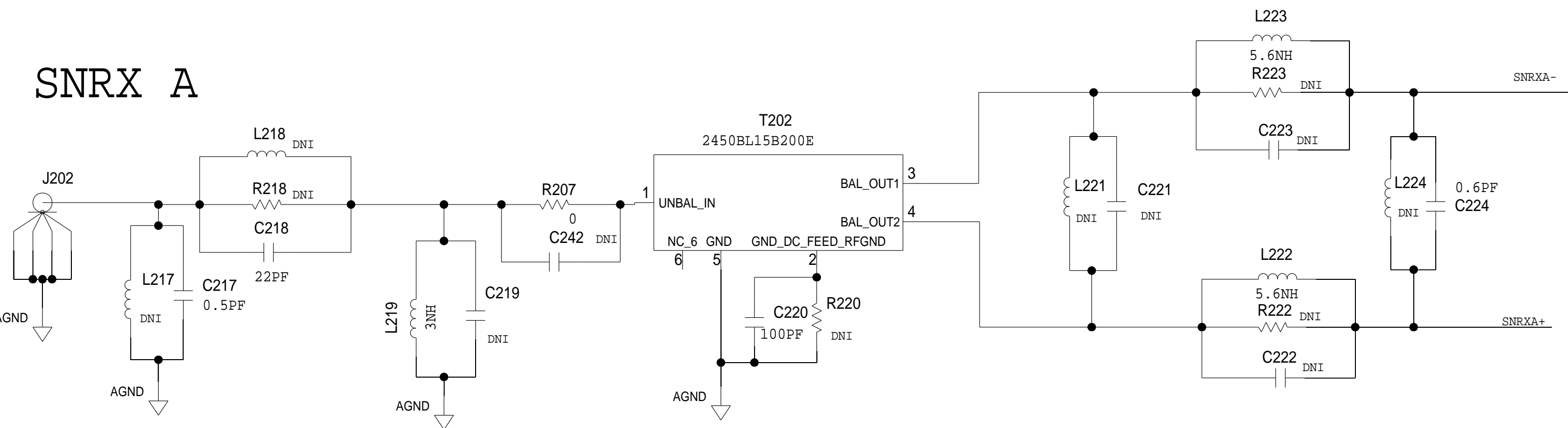
RX2 INPUT



ORX2_INPUT



SNRX A



ANALOG
DEVICES

SCHEMATIC

HW TYPE : Customer Evaluation
Product(s): AD9375

DESIGN VIEW
<DESIGN_VIEW>

PTD ENGINEER
T KARPUS

DRAWING NO.
02_045800

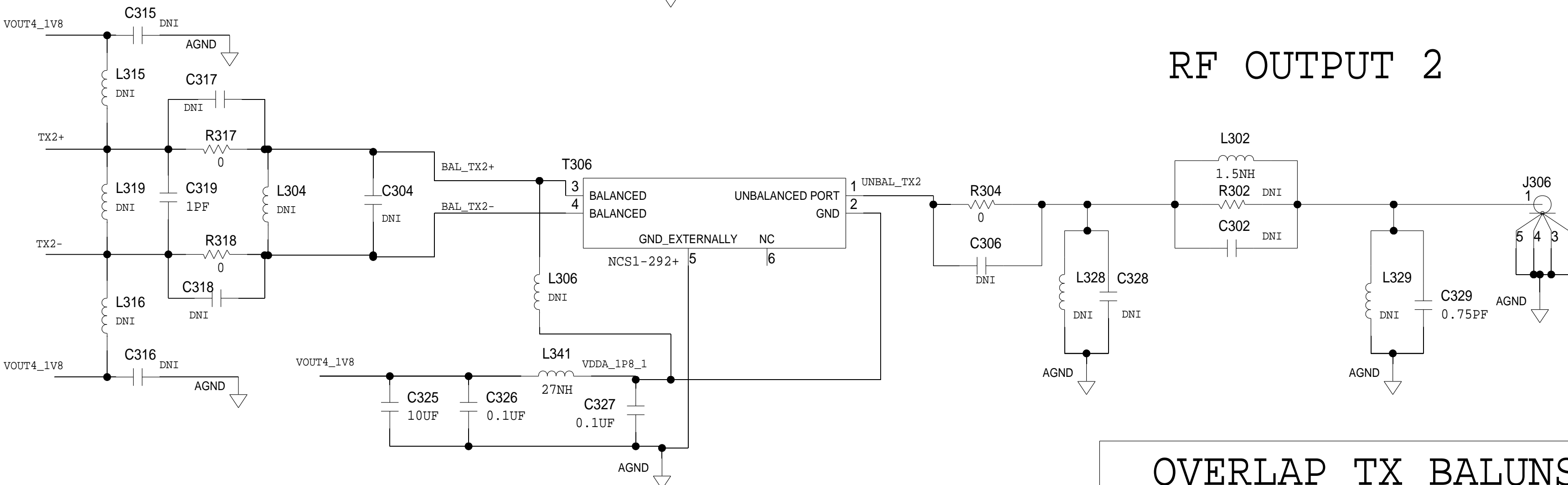
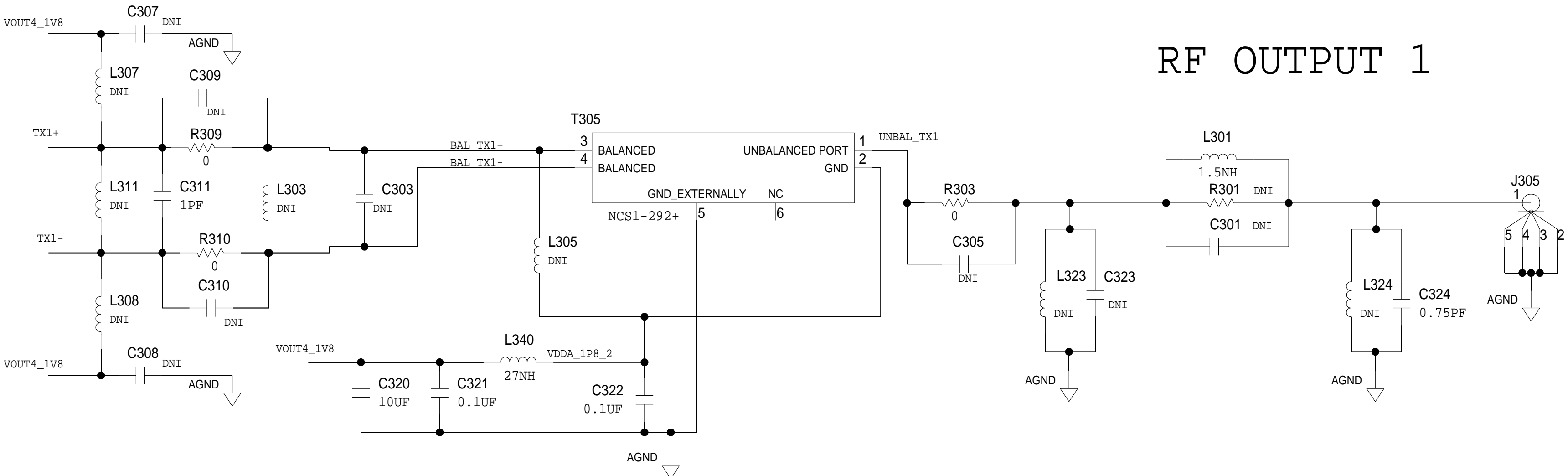
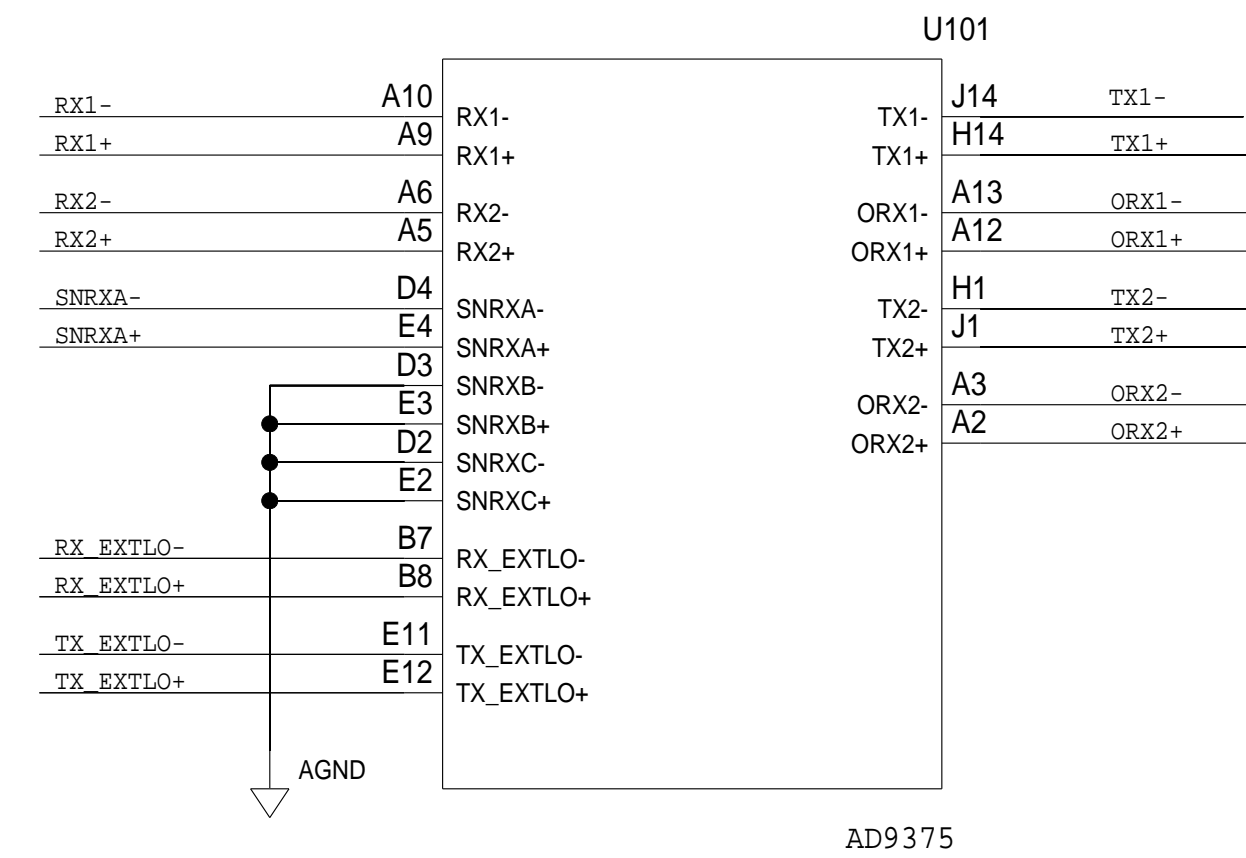
SIZE
D 1:1

REV
A

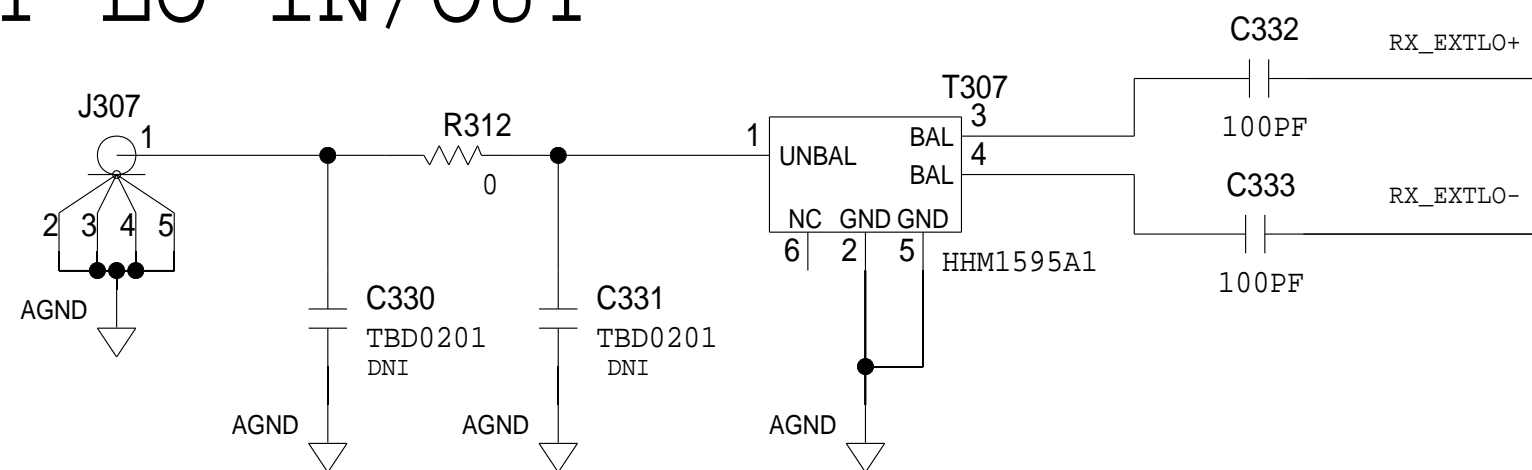
SHEET 2 OF 8

RF TX OUT / EXT LO IN

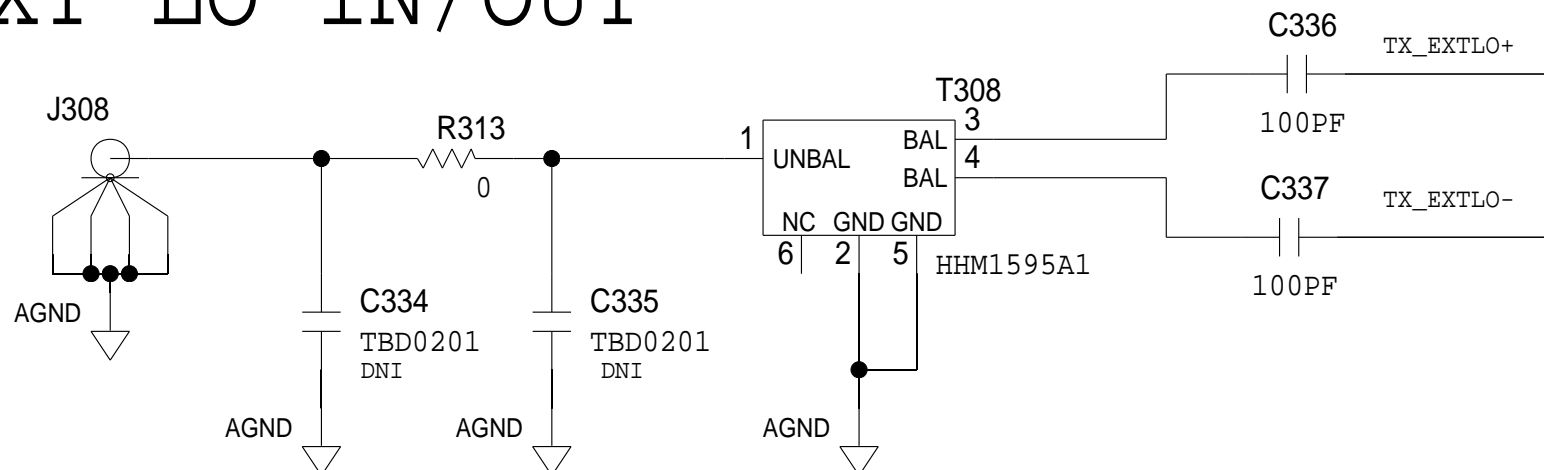
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



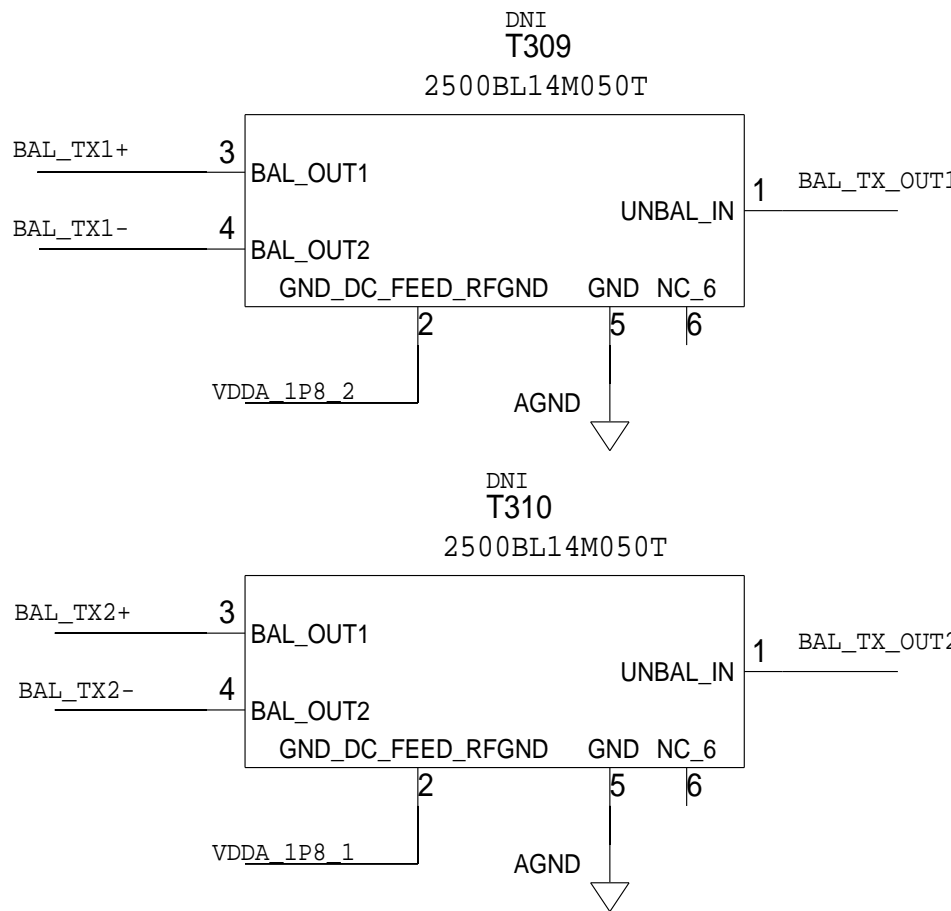
RX EXT LO IN/OUT



TX EXT LO IN/OUT

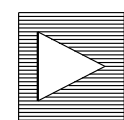


OVERLAP TX BALUNS



NOTE

EACH LC OR RLC CLUSTER REPRESENTS A SINGLE PAD WHICH CAN BE POPULATED BY ONE OR NONE OF THE R,L OR C COMPONENTS.



ANALOG
DEVICES

SCHEMATIC

HW TYPE : Customer Evaluation
Product(s): AD9375

DESIGN VIEW

<DESIGN_VIEW>

PTD ENGINEER

T KARPUS

DRAWING NO.

02_045800

SIZE

D

SCALE

1:1

REV

A

SHEET

3

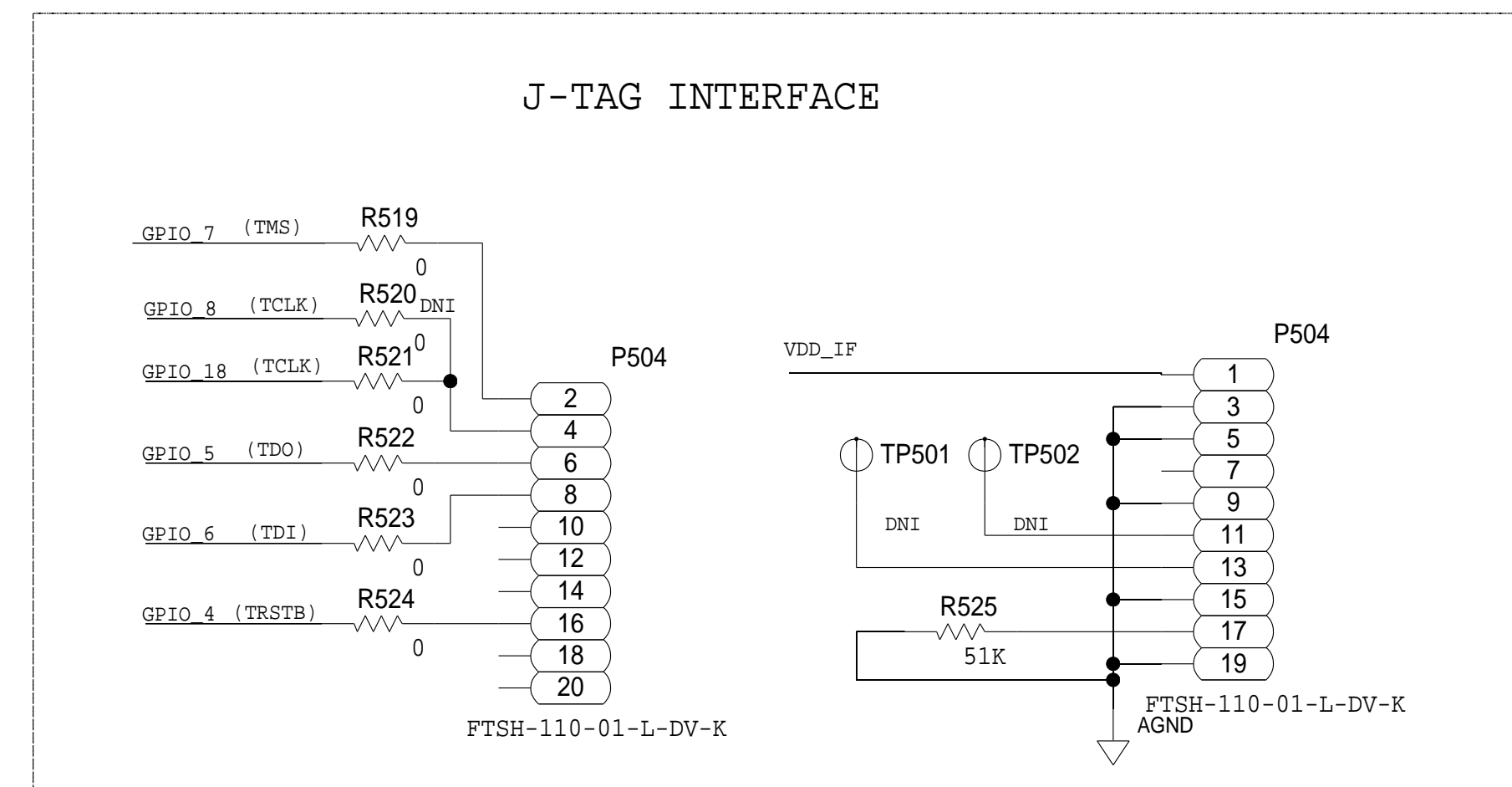
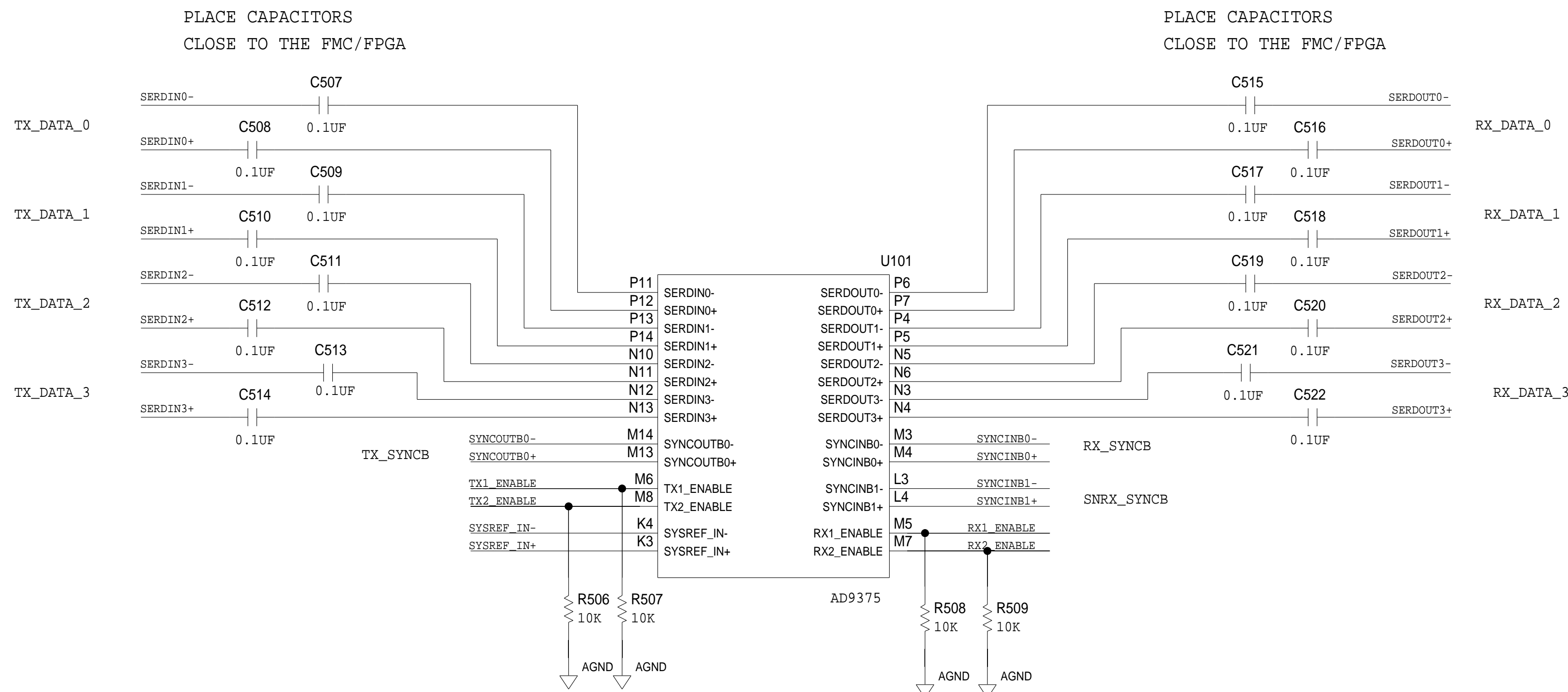
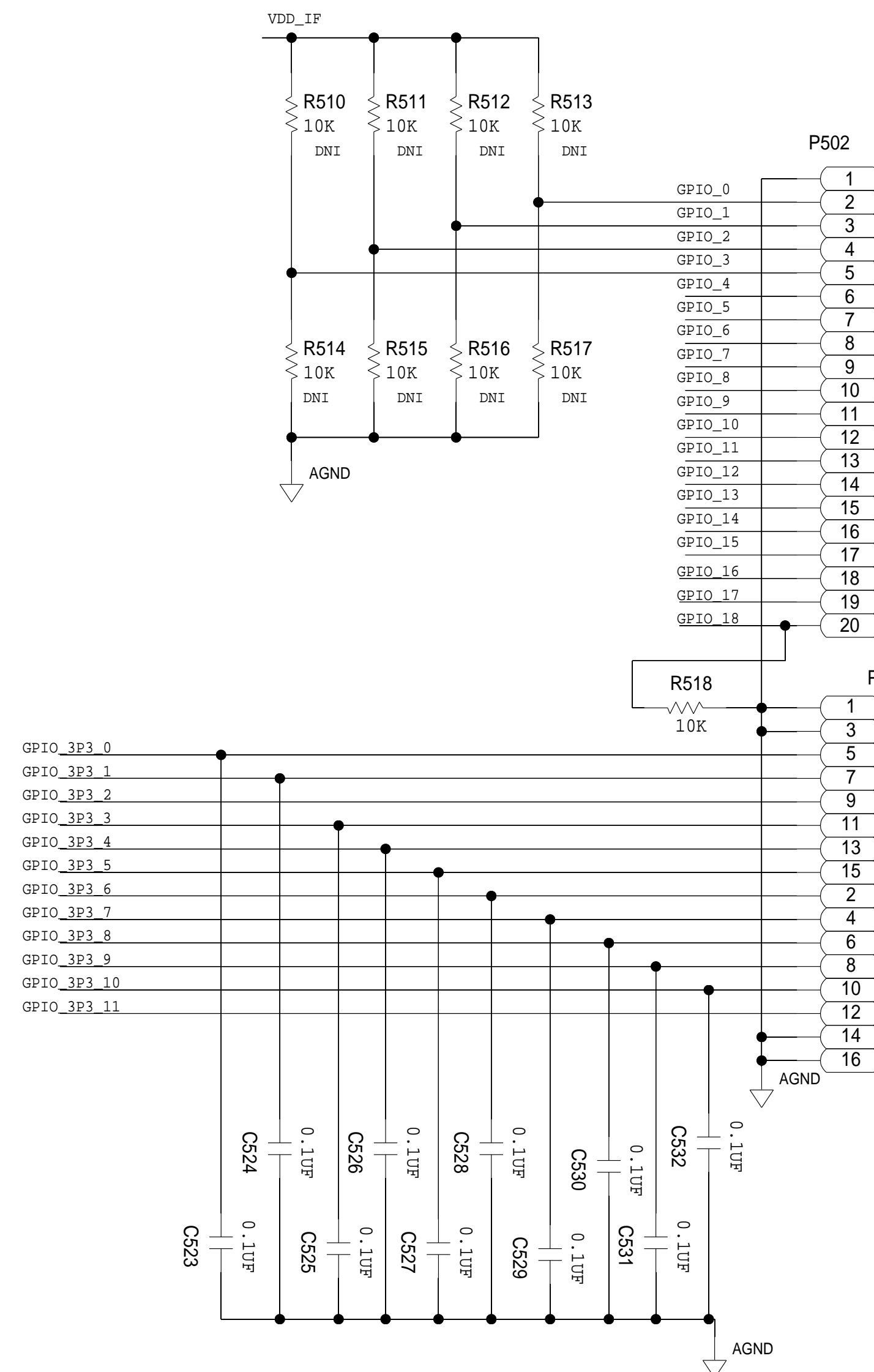
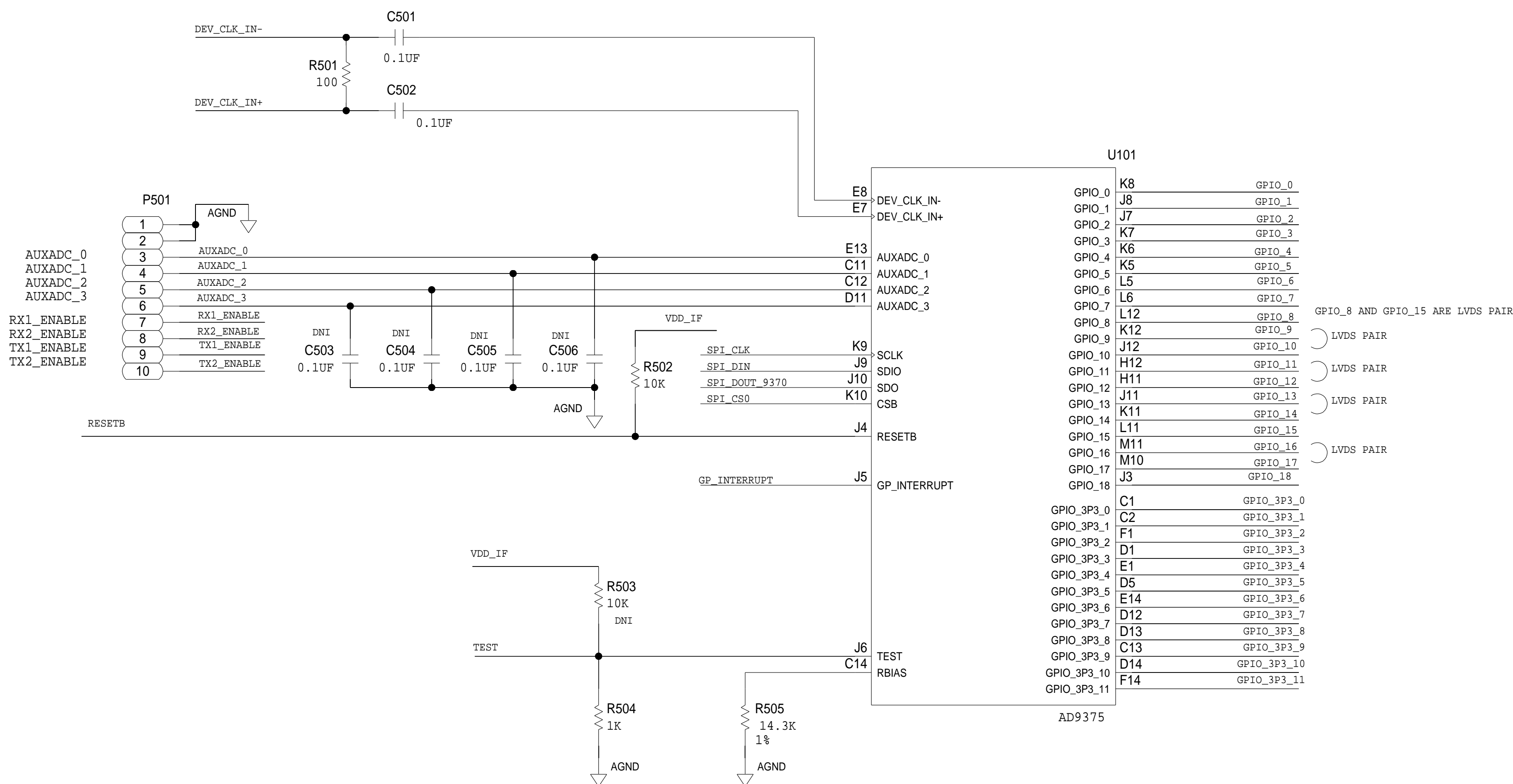
OF

8

JESD INTERFACE / GPIO / JTAG

REVISIONS

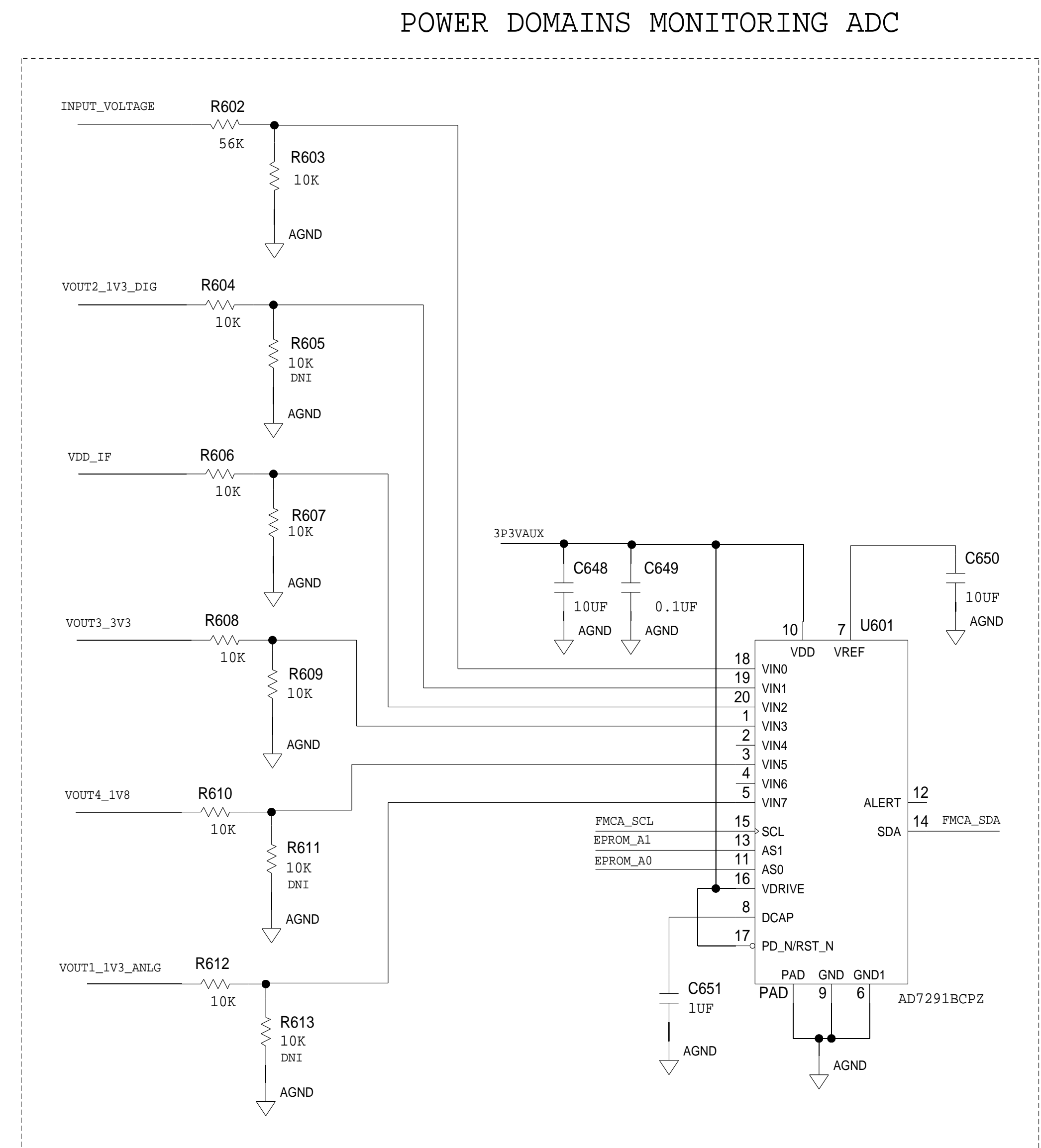
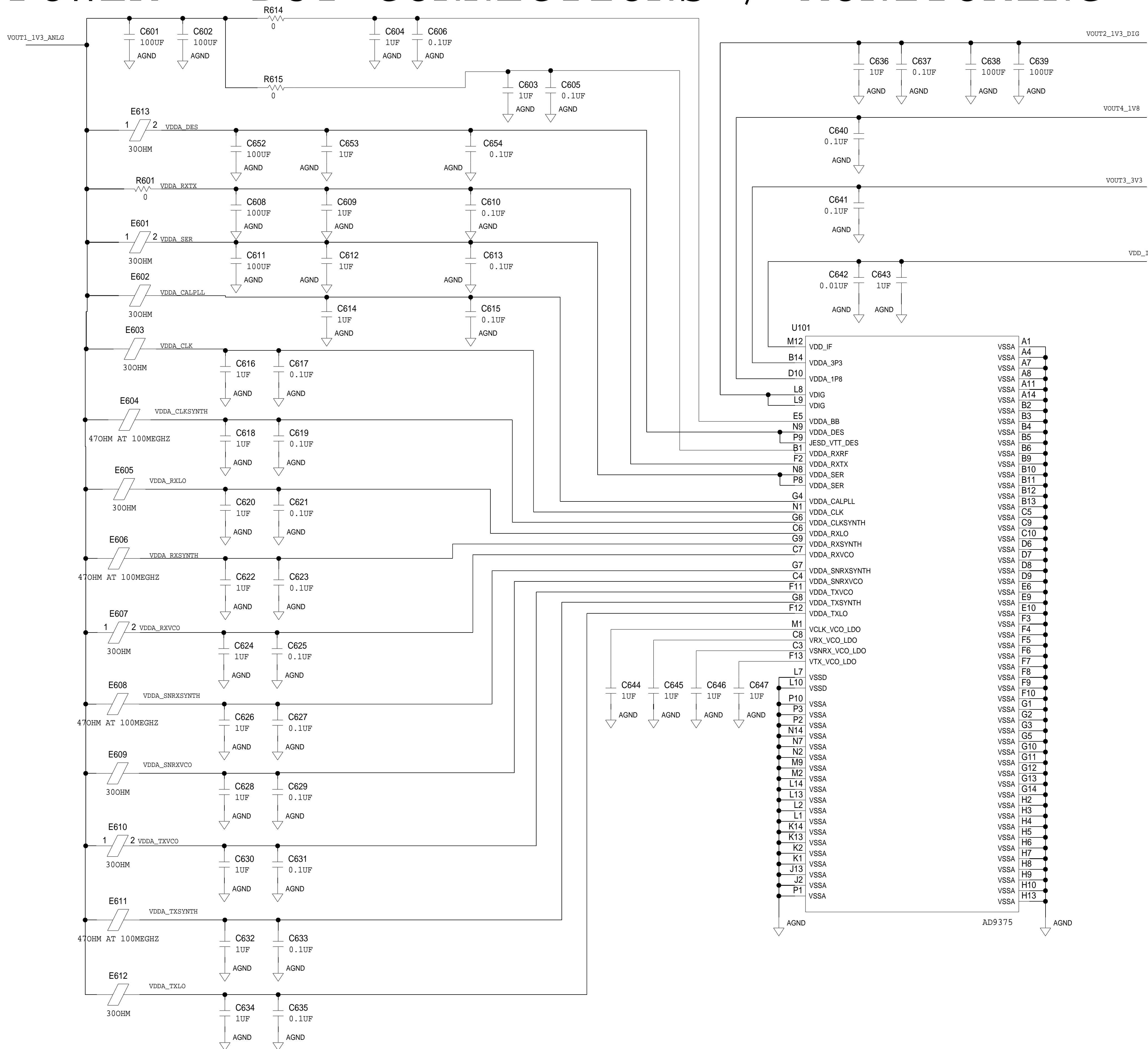
REV	DESCRIPTION	DATE	APPROVED



SCHEMATIC			
HW TYPE : Customer Evaluation Product(s): AD9375			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_045800	REV A	
PTD ENGINEER T KARPUS	SIZE D	SCALE 1:1	SHEET 5 OF 8

POWER - DUT CONNECTIONS / MONITORING

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

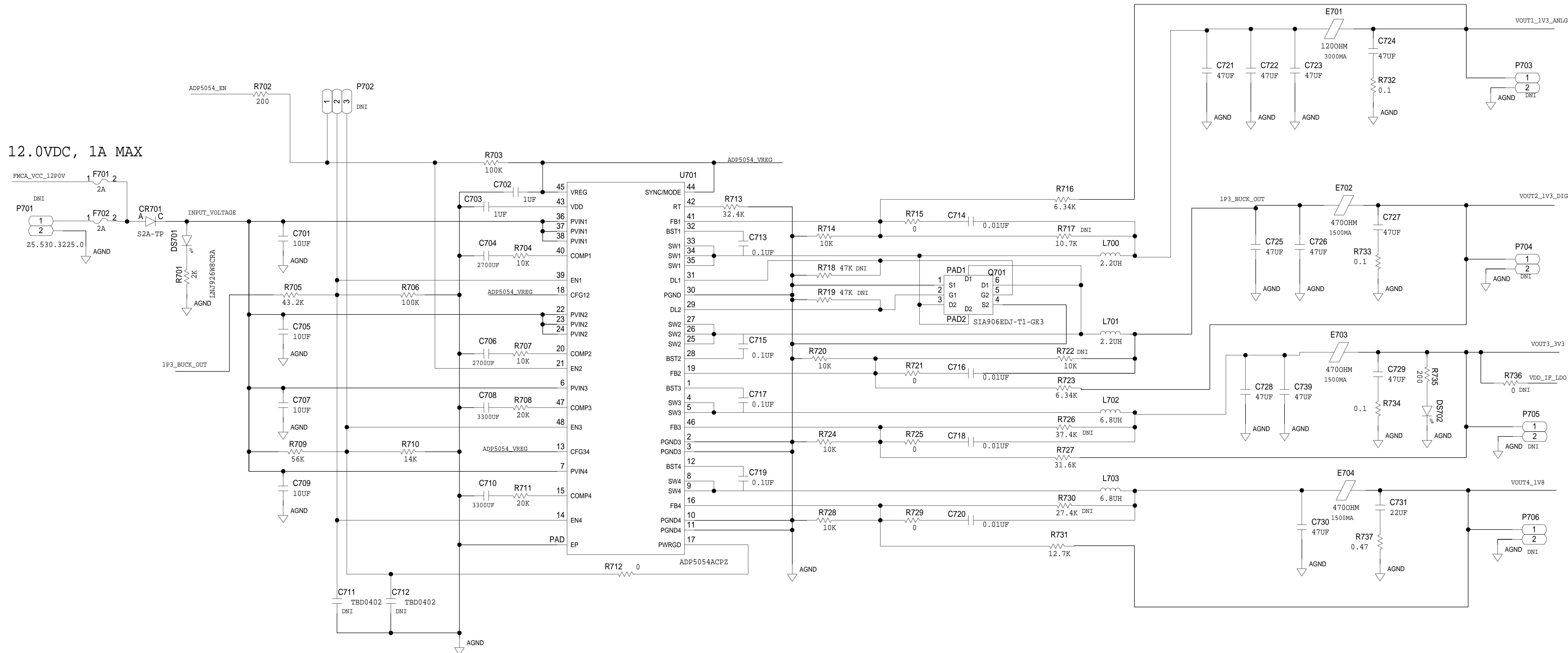


SCHEMATIC			
HW TYPE : Customer Evaluation Product(s): AD9375			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_045800	REV A	
PTD ENGINEER T KARPUS	SIZE D	SCALE 1:1	SHEET 6 OF 8

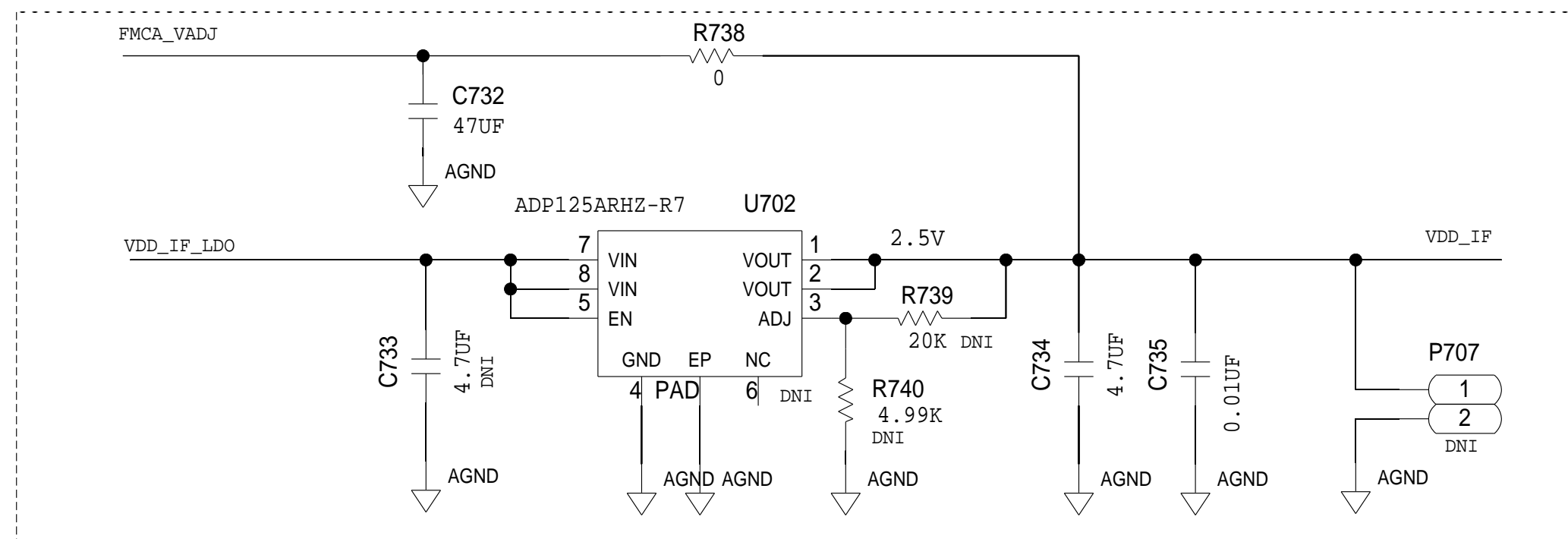
POWER SUPPLY

REVISIONS

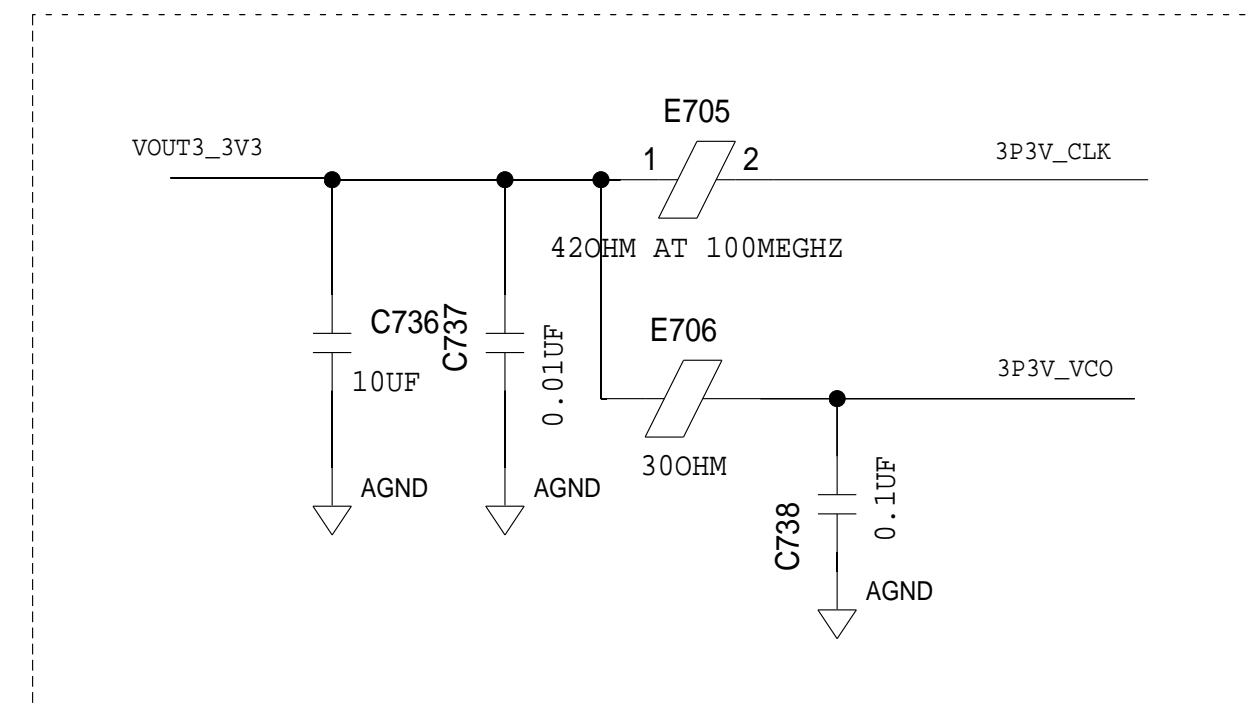
REV	DESCRIPTION	DATE	APPROVED



INTERFACE SUPPLY



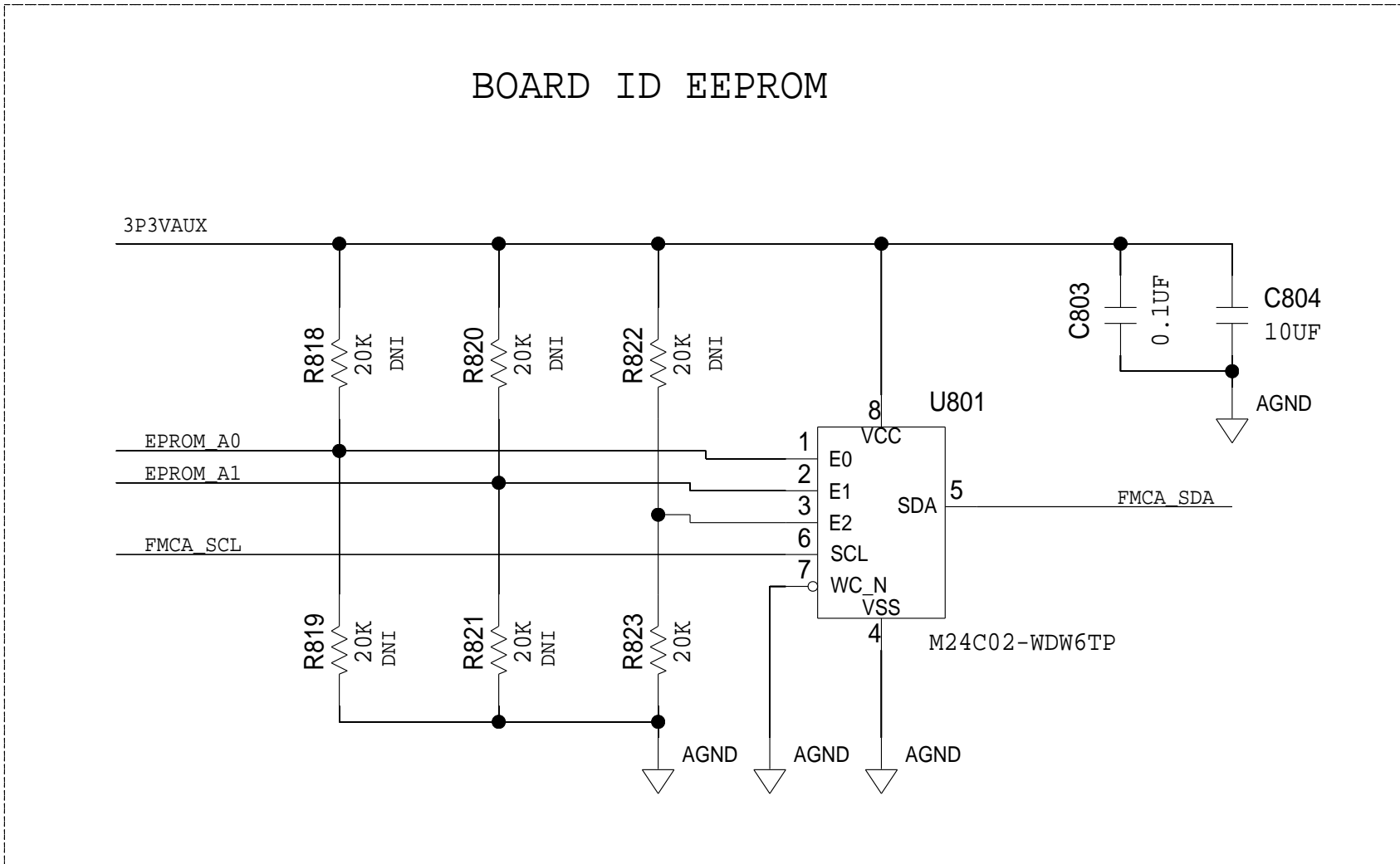
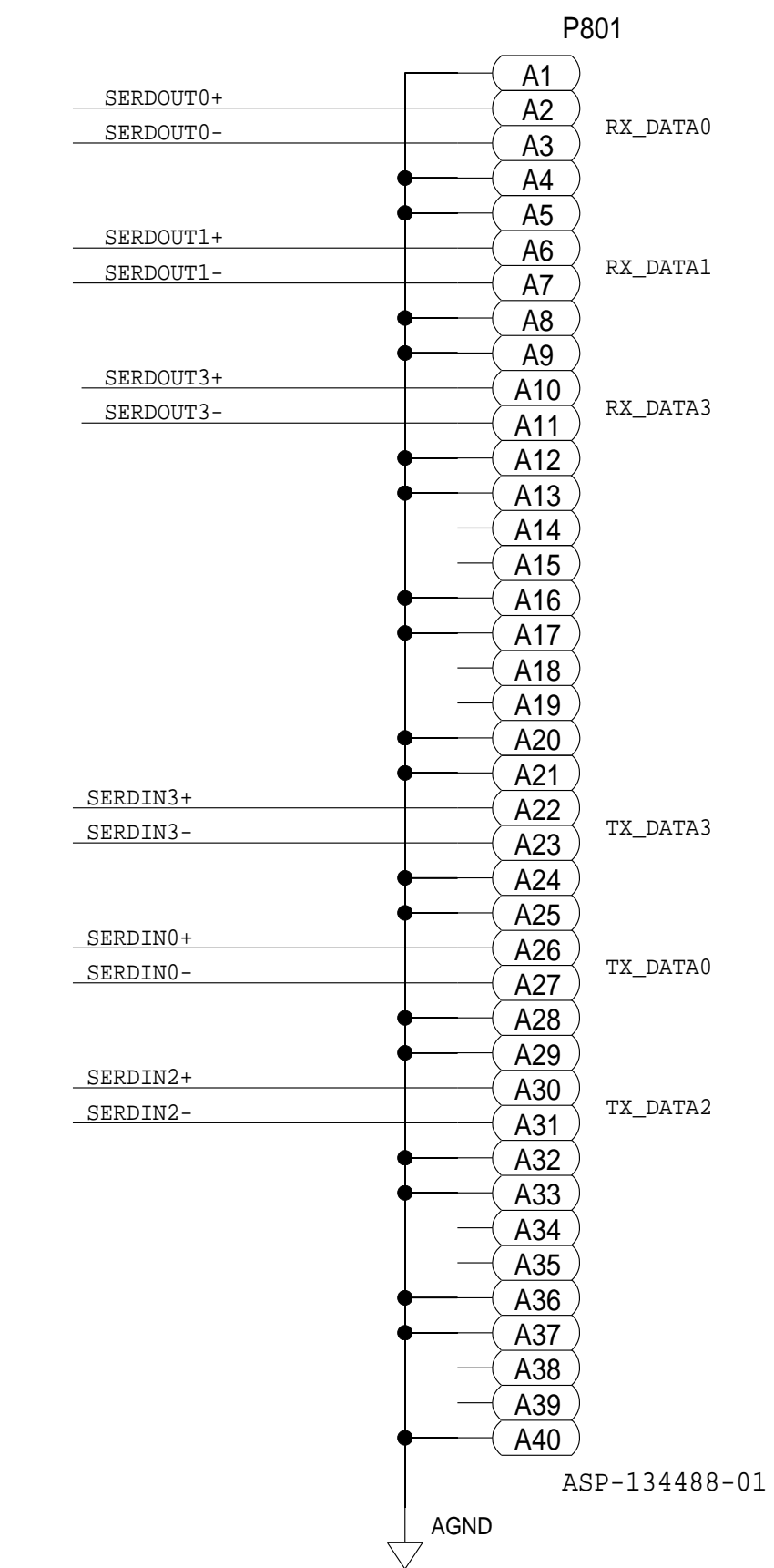
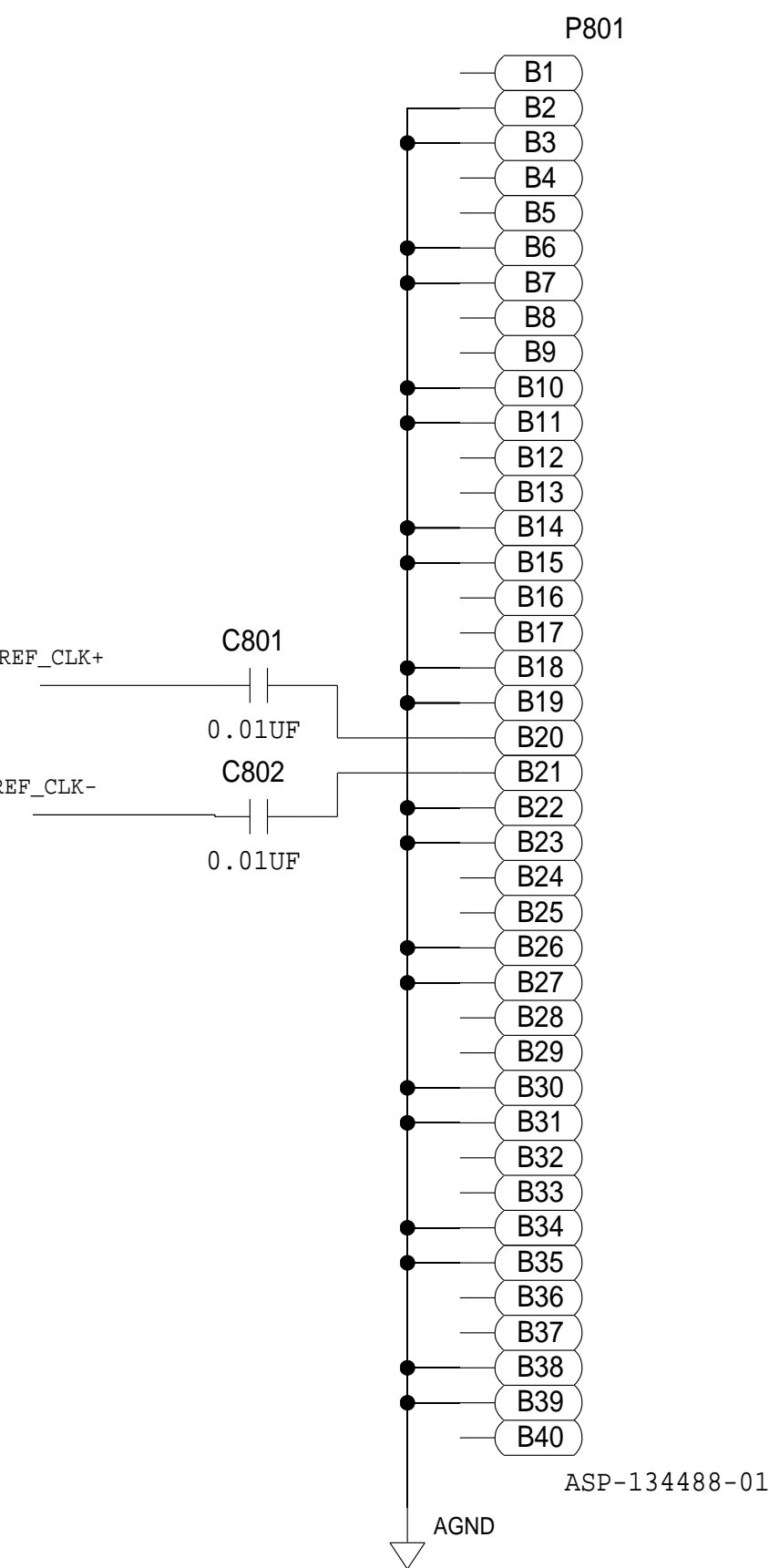
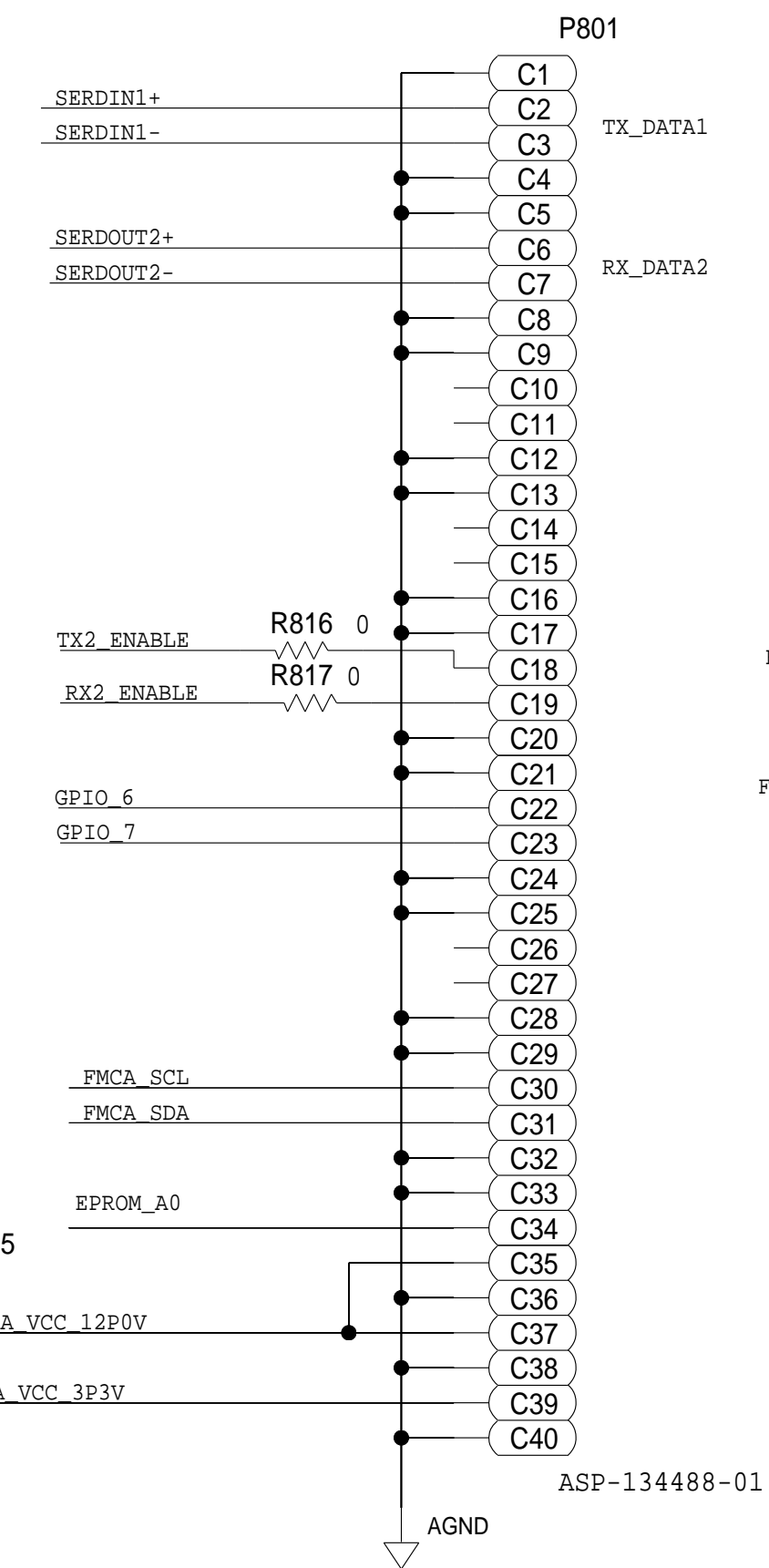
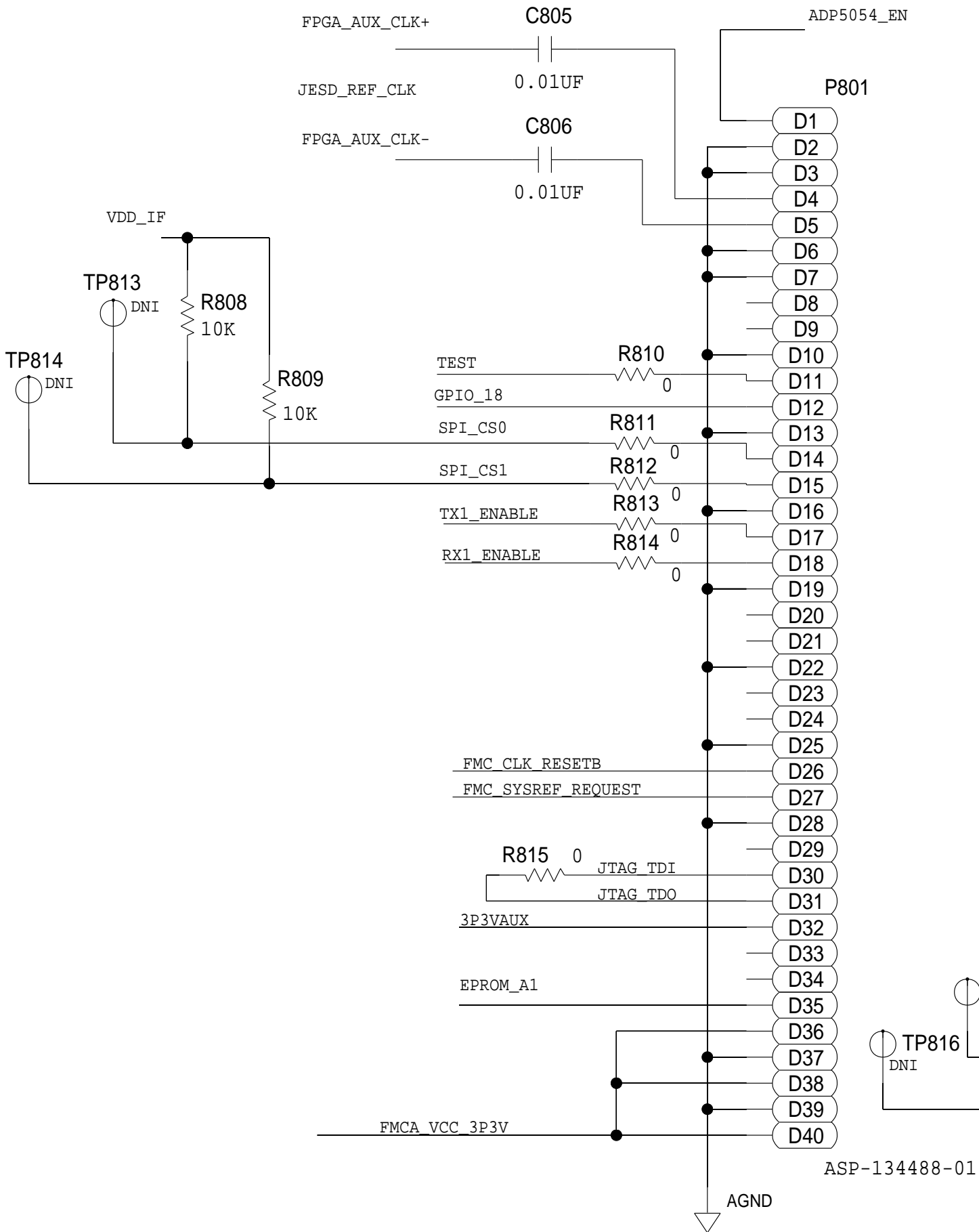
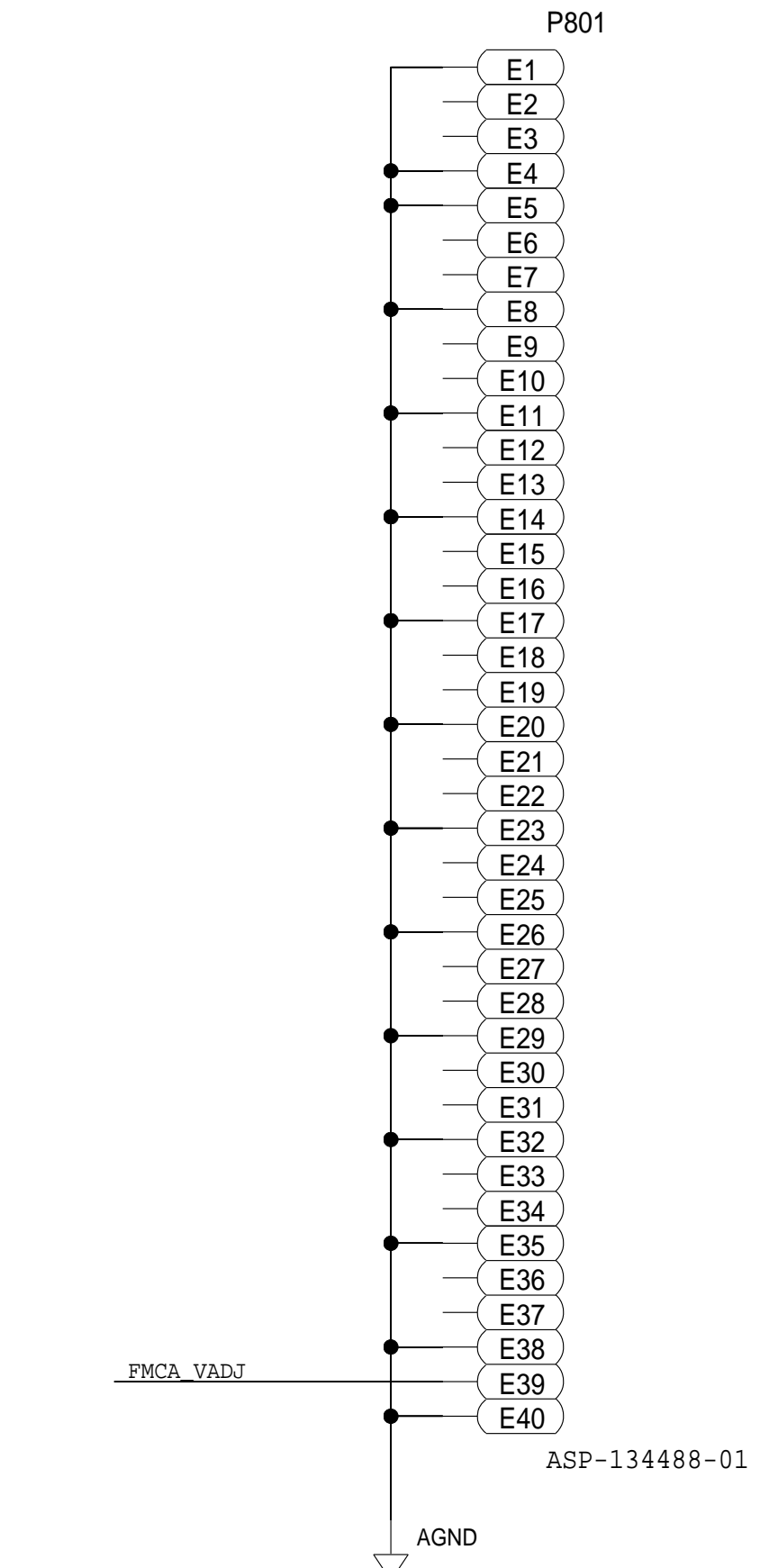
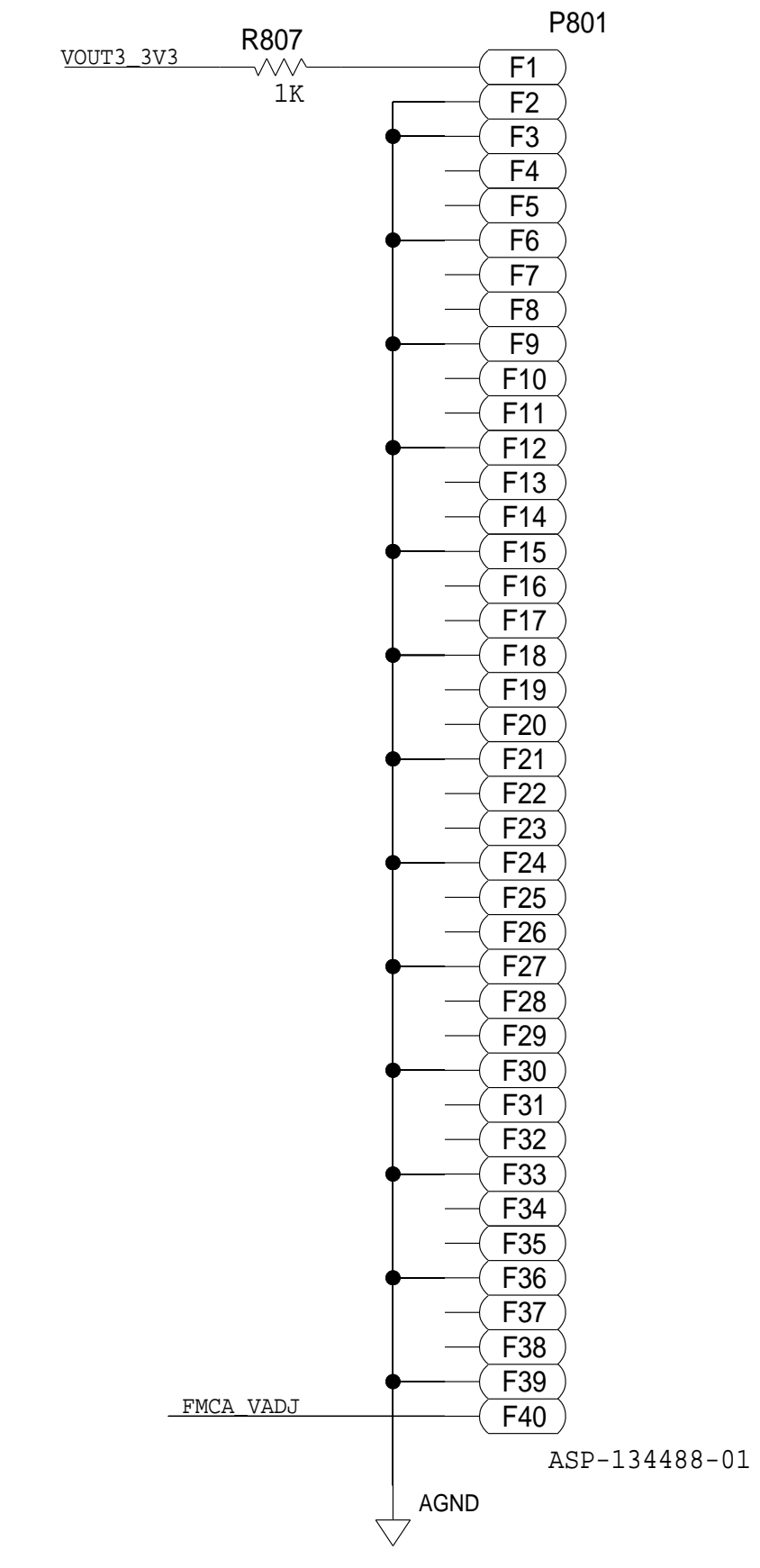
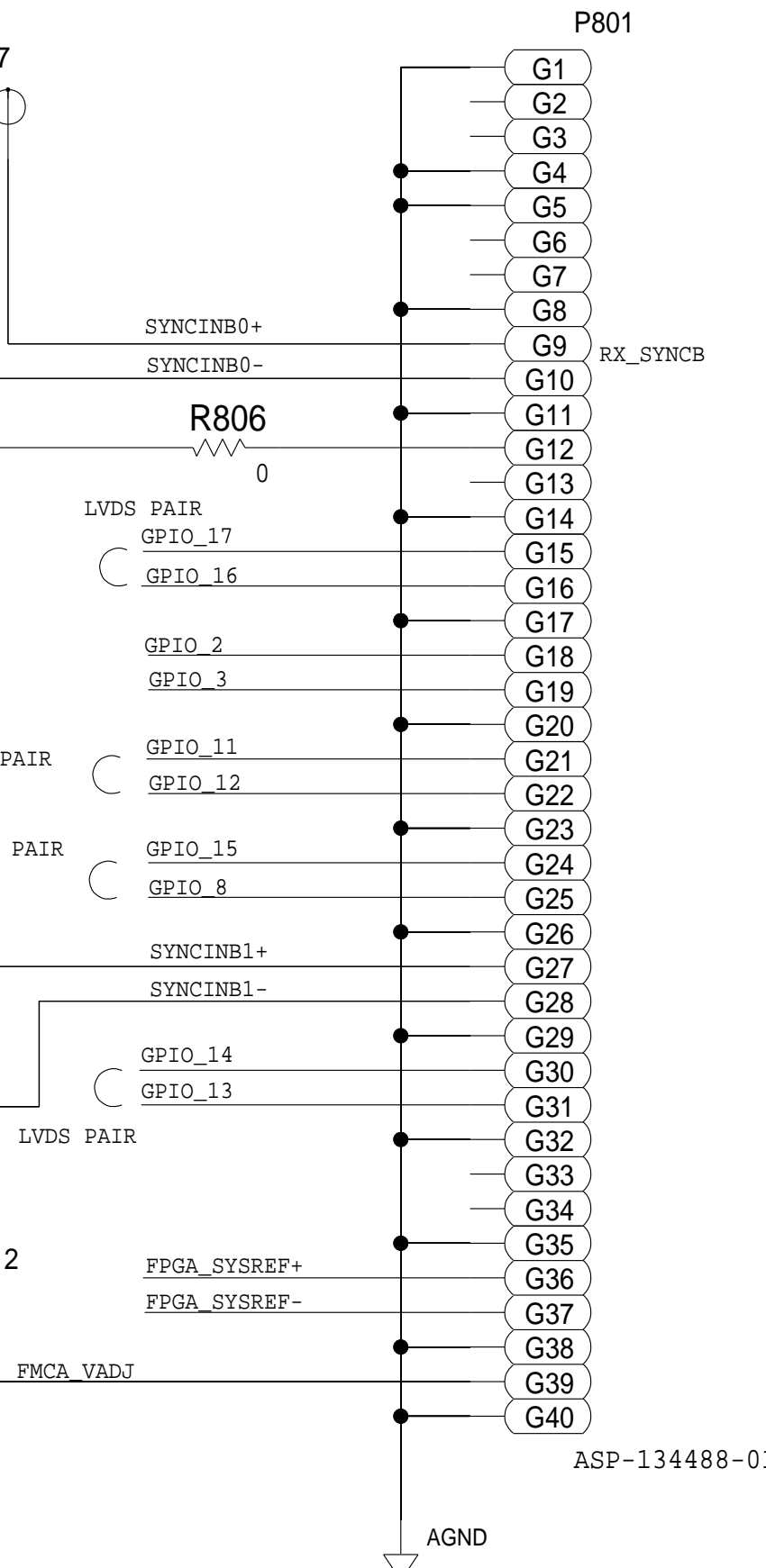
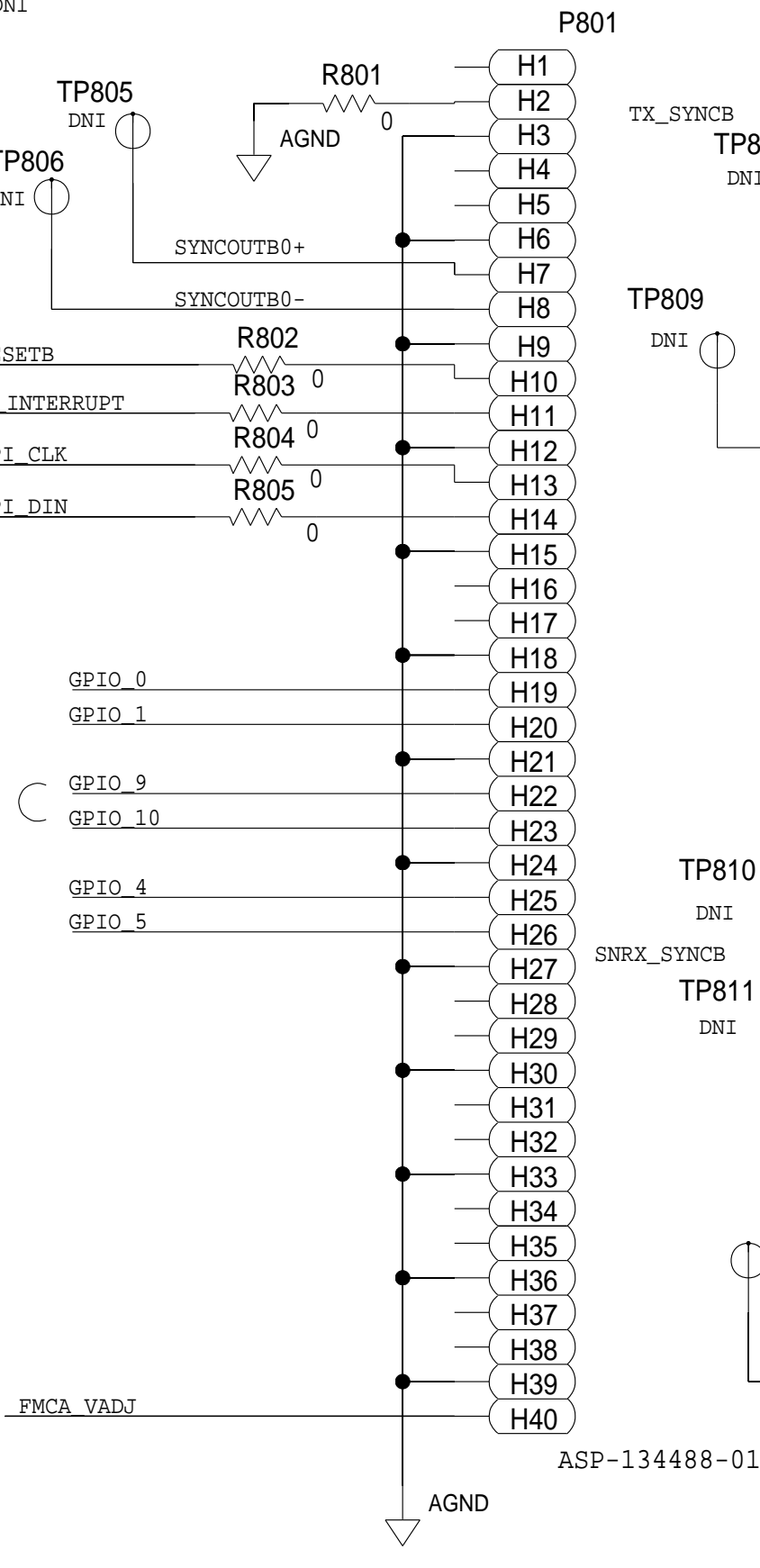
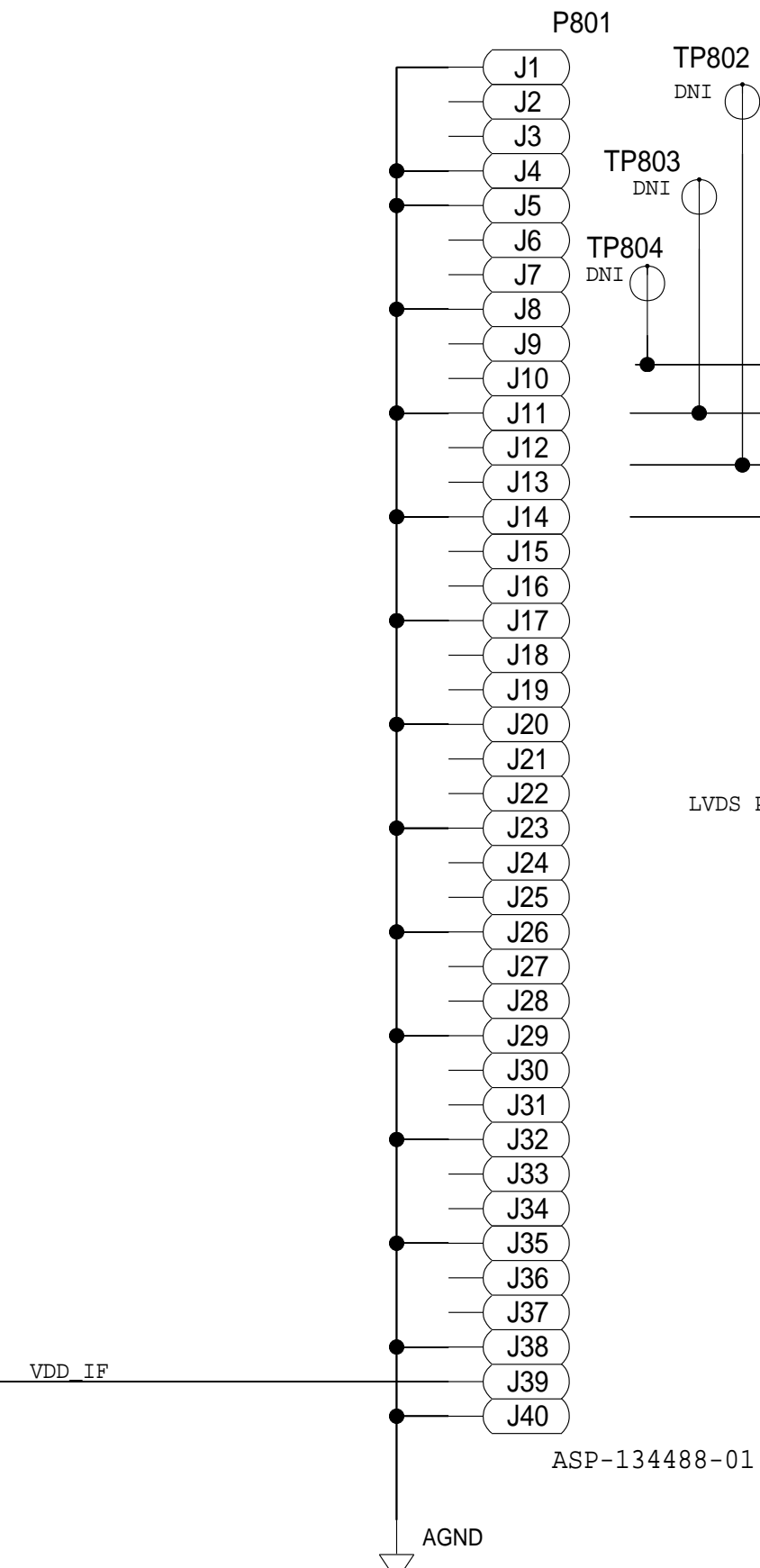
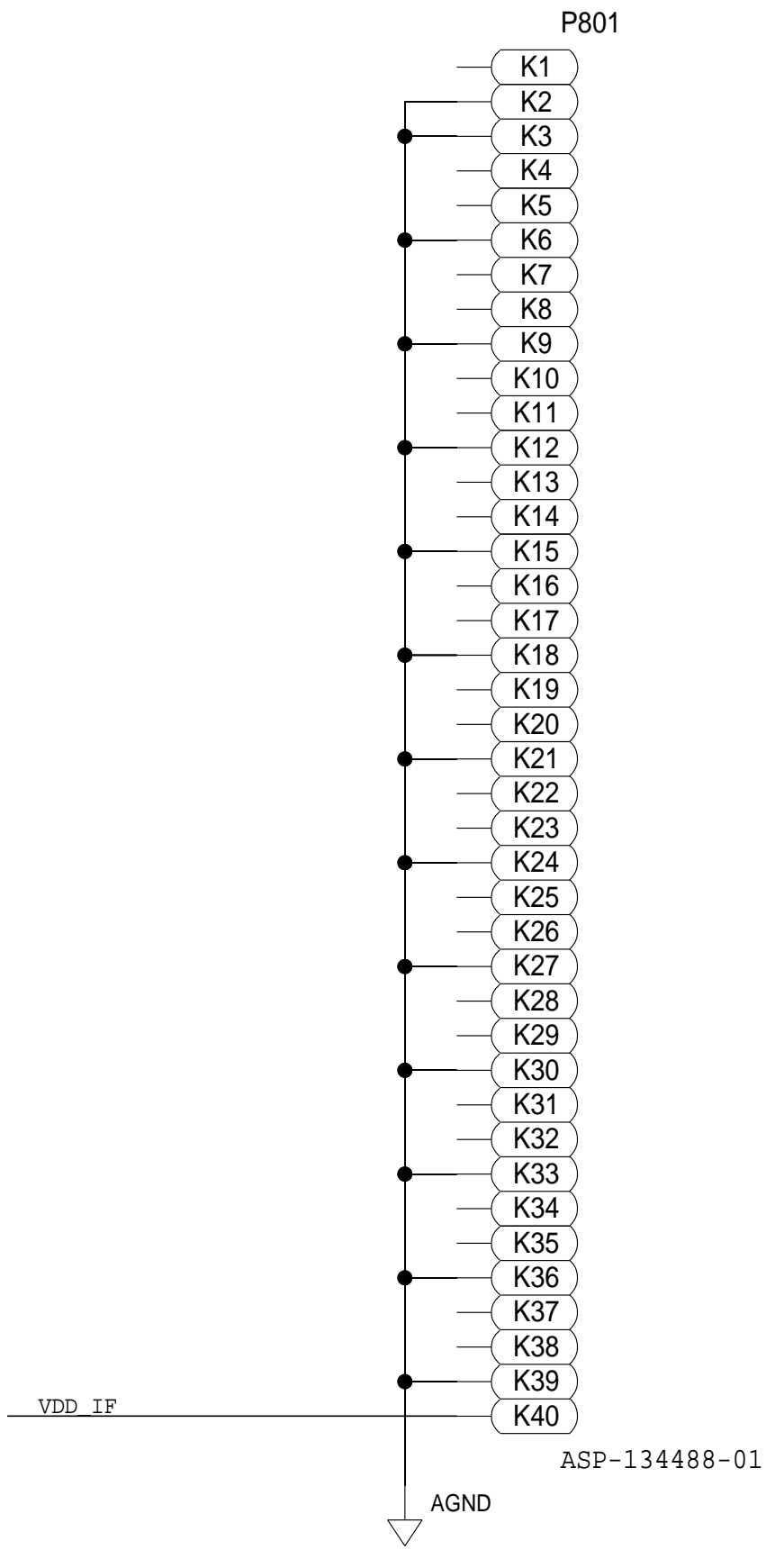
CLOCK SUPPLY



SCHEMATIC			
HW TYPE : Customer Evaluation Product(s): AD9375			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_045800	REV A	
PTD ENGINEER T KARPUS	SIZE D	SCALE 1:1	SHEET 7 OF 8

FMC - FPGA CONNECTIONS

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



SCHEMATIC			
HW TYPE : Customer Evaluation Product(s): AD9375			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_045800	REV A	
PTD ENGINEER T KARPUS	SIZE D	SCALE 1:1	SHEET 8 OF 8